## Thermally-Enhanced High Power RF LDMOS FET <br> 150 W, 920 - 960 MHz

## Description

The PTFA091503EL is a 150-watt, internally-matched FET intended for use in power amplifier applications in the 920 to 960 MHz band. This device features internal I/O matching and thermally-enhanced open cavity ceramic package. Manufactured with Infineon's advanced LDMOS process, this device provides excellent thermal performance and superior reliability.

## Two-carrier WCDMA Performance

$\mathrm{V}_{\mathrm{DD}}=30 \mathrm{~V}, \mathrm{I}_{\mathrm{DQ}}=1250 \mathrm{~mA}, f=960 \mathrm{MHz}, 3 \mathrm{GPP}$ WCDMA signal, $\mathrm{PAR}=8 \mathrm{~dB}, 10 \mathrm{MHz}$ carrier spacing, 3.84MHz Bandwidth


PTFA091503EL
Package H-33288-6


## Features

- Broadband internal matching
- Typical two-carrier WCDMA performance at $960 \mathrm{MHz}, 30 \mathrm{~V}$
- Average output power = 32 W
- Linear Gain $=17 \mathrm{~dB}$
- Efficiency = 29\%
- Intermodulation distortion $=-37 \mathrm{dBc}$
- Adjacent channel power $=-39 \mathrm{dBc}$
- Typical CW performance, $960 \mathrm{MHz}, 30 \mathrm{~V}$
- Output power at $\mathrm{P}_{1 \mathrm{~dB}}=150 \mathrm{~W}$
- Linear Gain = 17 dB
- Efficiency = 54\%
- Integrated ESD protection: Human Body Model, Class 2 (minimum)
- Excellent thermal stability, low HCl drift
- Capable of handling 10:1 VSWR @ 30 V, 150 W (CW) output power
- Pb-free, RoHS-compliant


## RF Characteristics

Two-carrier WCDMA Measurements (not subject to production test-verified by design/characterization in Infineon test fixture)
$\mathrm{V}_{\mathrm{DD}}=30 \mathrm{~V}, \mathrm{I}_{\mathrm{DQ}}=1250 \mathrm{~mA}, \mathrm{P}_{\mathrm{OUT}}=32 \mathrm{~W}$ average
$f_{1}=950 \mathrm{MHz}, f_{2}=960 \mathrm{MHz}, 3 \mathrm{GPP}$ signal, channel bandwidth $=3.84 \mathrm{MHz}$, peak/average $=8 \mathrm{~dB} @ 0.01 \%$ CCDF

| Characteristic | Symbol | Min | Typ | Max | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Gain | $\mathrm{G}_{\mathrm{ps}}$ | - | 17 | - | dB |
| Drain Efficiency | $\eta \mathrm{D}$ | - | 29 | - | $\%$ |
| Intermodulation Distortion | IMD | - | -37 | - | dBc |

All published data at $T_{\text {CASE }}=25^{\circ} \mathrm{C}$ unless otherwise indicated
ESD: Electrostatic discharge sensitive device-observe handling precautions!

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## RF Characteristics (cont.)

Two-tone Measurements (tested in Infineon test fixture)
$\mathrm{V}_{\mathrm{DD}}=30 \mathrm{~V}, \mathrm{I}_{\mathrm{DQ}}=1250 \mathrm{~mA}, \mathrm{P}_{\text {OUT }}=140 \mathrm{~W}$ PEP, $f=960 \mathrm{MHz}$, tone spacing $=1 \mathrm{MHz}$

| Characteristic | Symbol | Min | Typ | Max | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Gain | Gps | 16 | 17 | - | dB |
| Drain Efficiency | $\eta D$ | 40 | 42 | - | $\%$ |
| Intermodulation Distortion | IMD | - | -30 | -28 | dBc |

## DC Characteristics

| Characteristic | Conditions | Symbol | Min | Typ | Max | Unit |
| :--- | :--- | :---: | :---: | :---: | :---: | :---: |
| Drain-Source Breakdown Voltage | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{DS}}=10 \mathrm{~mA}$ | $\mathrm{~V}_{(\mathrm{BR}) \mathrm{DSS}}$ | 65 | - | - | V |
| Drain Leakage Current | $\mathrm{V}_{\mathrm{DS}}=28 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ | $\mathrm{I}_{\mathrm{DSS}}$ | - | - | 1.0 | $\mu \mathrm{~A}$ |
|  | $\mathrm{~V}_{\mathrm{DS}}=63 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ | $\mathrm{I}_{\mathrm{DSS}}$ | - | - | 10.0 | $\mu \mathrm{~A}$ |
| On-State Resistance | $\mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0.1 \mathrm{~V}$ | $\mathrm{R}_{\mathrm{DS}(\mathrm{on})}$ | - | 0.07 | - | $\Omega$ |
| Operating Gate Voltage | $\mathrm{V}_{\mathrm{DS}}=30 \mathrm{~V}, \mathrm{I}_{\mathrm{DQ}}=1250 \mathrm{~mA}$ | $\mathrm{~V}_{\mathrm{GS}}$ | 2.0 | 2.5 | 3.0 | V |
| Gate Leakage Current | $\mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0 \mathrm{~V}$ | $\mathrm{I}_{\mathrm{GSS}}$ | - | - | 1.0 | $\mu \mathrm{~A}$ |

## Maximum Ratings

| Parameter | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Drain-Source Voltage | $\mathrm{V}_{\mathrm{DSS}}$ | 65 | V |
| Gate-Source Voltage | $\mathrm{V}_{\mathrm{GS}}$ | -0.5 to +12 | V |
| Junction Temperature | $\mathrm{T}_{\mathrm{J}}$ | 200 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $\mathrm{T}_{\text {STG }}$ | -40 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Thermal Resistance $\left(\mathrm{T}_{\mathrm{CASE}}=70^{\circ} \mathrm{C}, 150 \mathrm{~W}\right.$ CW $)$ | $\mathrm{R}_{\theta \mathrm{CC}}$ | 0.42 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

## Ordering Information

| Type and Version | Package Outline | Package Description | Shipping |
| :--- | :--- | :--- | :--- |
| PTFA091503EL V4 | $\mathrm{H}-33288-6$ | Thermally-enhanced slotted flange, single-ended | Tray |
| PTFA091503EL V4 R250 | $\mathrm{H}-33288-6$ | Thermally-enhanced slotted flange, single-ended | Tape \& Reel, 250 pcs |

## Typical Performance






Typical Performance (cont.)


## Broadband Circuit Impedance



| Frequency | Z Source $\Omega$ |  | Z Load $\Omega$ |  |
| :---: | :---: | :---: | :---: | :---: |
| $\mathbf{M H z}$ | $\mathbf{R}$ | $\mathbf{j X}$ | $\mathbf{R}$ | $\mathbf{j X}$ |
| 910 | 1.08 | -1.2 | 2.88 | -1.0 |
| 920 | 1.12 | -0.9 | 2.87 | -0.7 |
| 930 | 1.15 | -0.8 | 2.87 | -0.6 |
| 940 | 1.16 | -0.6 | 2.88 | -0.4 |
| 950 | 1.21 | -0.4 | 2.88 | -0.3 |
| 960 | 1.28 | -0.3 | 2.9 | -0.1 |
| 970 | 1.33 | -0.2 | 2.9 | 0.1 |



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## Reference Circuit



Reference circuit input schematic for $f=960 \mathrm{MHz}$


Reference circuit output schematic for $f=960 \mathrm{MHz}$

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## Reference Circuit (cont.)

| Description |  |
| :--- | :--- |
| DUT | PTFA091503EL |
| PCB | $0.76 \mathrm{~mm}[.030 \mathrm{O}]$ thick, $\varepsilon r=3.48$, Rogers 4350, 1 oz. copper |

## Electrical Characteristics at 960 MHz

| Transmission Line | Electrical <br> Characteristics | Dimensions: mm | Dimensions: mils |
| :---: | :---: | :---: | :---: |
| Input |  |  |  |
| TL101 | $0.098 \lambda, 78.27 \Omega$ | $\mathrm{W}=0.762, \mathrm{~L}=19.050$ | $\mathrm{W}=30, \mathrm{~L}=750$ |
| TL101 | $0.098 \lambda, 78.27 \Omega$ | $\mathrm{W}=0.762, \mathrm{~L}=19.050$ | $\mathrm{W}=30, \mathrm{~L}=750$ |
| TL102 | $0.004 \lambda, 51.58 \Omega$ | W = 1.651, L = 0.762 | $\mathrm{W}=65, \mathrm{~L}=30$ |
| TL103 | $0.026 \lambda, 78.27 \Omega$ | W = 0.762, L = 5.080 | $\mathrm{W}=30, \mathrm{~L}=200$ |
| TL104 | $0.001 \lambda, 36.29 \Omega$ | $\mathrm{W}=2.794, \mathrm{~L}=0.254$ | $\mathrm{W}=110, \mathrm{~L}=10$ |
| TL105 | $0.039 \lambda, 8.94 \Omega$ | $\mathrm{W}=15.240, \mathrm{~L}=6.731$ | $\mathrm{W}=600, \mathrm{~L}=265$ |
| TL106 | $0.034 \lambda, 51.58 \Omega$ | $\mathrm{W}=1.651, \mathrm{~L}=6.375$ | $\mathrm{W}=65, \mathrm{~L}=251$ |
| TL107 | $0.001 \lambda, 36.29 \Omega$ | $\mathrm{W}=2.794, \mathrm{~L}=0.254$ | $\mathrm{W}=110, \mathrm{~L}=10$ |
| TL108 | $0.007 \lambda, 51.58 \Omega$ | $\mathrm{W}=1.651, \mathrm{~L}=1.270$ | $\mathrm{W}=65, \mathrm{~L}=50$ |
| TL109, TL110, TL111, TL112 |  | $\mathrm{W}=1.651$ | $\mathrm{W}=65$ |
| TL113, TL114, TL115, TL116, TL117 |  | $\mathrm{W}=0.762$ | $\mathrm{W}=30$ |
| TL118, TL119 | $0.014 \lambda, 36.29 \Omega$ | $\mathrm{W} 1=2.794, \mathrm{~W} 2=2.794, \mathrm{~W} 3=2.540$ | $\mathrm{W} 1=110, \mathrm{~W} 2=110, \mathrm{~W} 3=100$ |
| TL120, TL121 | $0.011 \lambda, 36.29 \Omega$ | $\mathrm{W} 1=2.794, \mathrm{~W} 2=2.794, \mathrm{~W} 3=2.032$ | $\mathrm{W} 1=110, \mathrm{~W} 2=110, \mathrm{~W} 3=80$ |
| TL122, TL124 | $0.016 \lambda, 36.29 \Omega$ | $\mathrm{W} 1=2.794, \mathrm{~W} 2=2.794, \mathrm{~W} 3=3.048$ | $\mathrm{W} 1=110, \mathrm{~W} 2=110, \mathrm{~W} 3=120$ |
| TL123, TL137 | $0.015 \lambda, 8.94 \Omega$ | $\mathrm{W} 1=15.240, \mathrm{~W} 2=15.240, \mathrm{~W} 3=2.540$ | $\mathrm{W} 1=600, \mathrm{~W} 2=600, \mathrm{~W} 3=100$ |
| TL125 | $0.004 \lambda, 8.94 \Omega$ | W 1 = 15.240, W2 $=15.240, \mathrm{~W} 3=0.762$ | $\mathrm{W} 1=600, \mathrm{~W} 2=600, \mathrm{~W} 3=30$ |
| TL126 |  | $\mathrm{W} 1=17.780, \mathrm{~W} 2=12.700$ | $\mathrm{W} 1=700, \mathrm{~W} 2=500$ |
| TL127 |  | $\mathrm{W} 1=2.540, \mathrm{~W} 2=15.240$ | $\mathrm{W} 1=100, \mathrm{~W} 2=600$ |
| TL128 | $0.003 \lambda, 78.27 \Omega$ | $\mathrm{W}=0.762, \mathrm{~L}=0.508$ | W = 30, L = 20 |
| TL129 | $0.033 \lambda, 8.94 \Omega$ | $\mathrm{W}=15.240, \mathrm{~L}=5.715$ | W = 600, L = 225 |
| TL130, TL132 | $0.040 \lambda, 51.58 \Omega$ | $\mathrm{W}=1.651, \mathrm{~L}=7.620$ | $\mathrm{W}=65, \mathrm{~L}=300$ |
| TL131 | $0.038 \lambda, 38.82 \Omega$ | $\mathrm{W}=2.540, \mathrm{~L}=7.112$ | W = 100, L = 280 |
| TL133 | $0.007 \lambda, 78.27 \Omega$ | $\mathrm{W}=0.762, \mathrm{~L}=1.270$ | W $=30, \mathrm{~L}=50$ |
| TL134 | $0.049 \lambda, 38.82 \Omega$ | $\mathrm{W}=2.540, \mathrm{~L}=9.144$ | W = 100, L = 360 |
| TL135 | $0.015 \lambda, 78.27 \Omega$ | $\mathrm{W}=0.762, \mathrm{~L}=2.921$ | $\mathrm{W}=30, \mathrm{~L}=115$ |
| TL136 | $0.012 \lambda, 8.94 \Omega$ | $\mathrm{W}=15.240, \mathrm{~L}=2.032$ | $\mathrm{W}=600, \mathrm{~L}=80$ |

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## Reference Circuit (cont.)

## Electrical Characteristics at 960 MHz

| Transmission Line | Electrical <br> Characteristics | Dimensions: mm | Dimensions: mils |
| :---: | :---: | :---: | :---: |
| Output |  |  |  |
| TL201, TL224 | $0.014 \lambda, 28.85 \Omega$ | $\mathrm{W} 1=3.810, \mathrm{~W} 2=3.810, \mathrm{~W} 3=2.540$ | W1 = 150, W2 = 150, W3 = 100 |
| TL202, TL229 | $0.007 \lambda, 28.85 \Omega$ | W = 3.810, L = 1.270 | W = 150, L = 50 |
| TL203, TL230 |  | $\begin{aligned} \mathrm{W} 1 & =3.810, \mathrm{~W} 2=1.829, \mathrm{~W} 3=3.810, \\ \mathrm{~W} 4 & =1.829 \end{aligned}$ | $\begin{aligned} & \mathrm{W} 1=150, \mathrm{~W} 2=72, \mathrm{~W} 3=150, \\ & \mathrm{~W} 4=72 \end{aligned}$ |
| TL204 | $0.010 \lambda, 28.85 \Omega$ | $\mathrm{W} 1=3.810, \mathrm{~W} 2=3.810, \mathrm{~W} 3=1.829$ | $\mathrm{W} 1=150, \mathrm{~W} 2=150, \mathrm{~W} 3=72$ |
| $\begin{aligned} & \text { TL205, TL221, TL222, } \\ & \text { TL223 } \end{aligned}$ |  | $\mathrm{W}=1.651$ | W = 65 |
| TL206 (taper) | $0.044 \lambda, 10.17 \Omega / 16.47 \Omega$ | $\mathrm{W} 1=13.208, \mathrm{~W} 2=7.620, \mathrm{~L}=7.620$ | $\mathrm{W} 1=520, \mathrm{~W} 2=300, \mathrm{~L}=300$ |
| TL207 | $0.004 \lambda, 51.58 \Omega$ | $\mathrm{W}=1.651, \mathrm{~L}=0.762$ | $\mathrm{W}=65, \mathrm{~L}=30$ |
| TL208, TL209 | $0.058 \lambda, 51.58 \Omega$ | $\mathrm{W}=1.651, \mathrm{~L}=10.922$ | $\mathrm{W}=65, \mathrm{~L}=430$ |
| TL210 | $0.015 \lambda, 51.58 \Omega$ | W = 1.651, L= 2.819 | $\mathrm{W}=65, \mathrm{~L}=111$ |
| TL211, TL212 | $0.000 \lambda, 146.88 \Omega$ | W = 0.025, L = 0.025 | $\mathrm{W}=1, \mathrm{~L}=1$ |
| TL213, TL233 | $0.093 \lambda, 28.85 \Omega$ | $\mathrm{W}=3.810, \mathrm{~L}=17.043$ | $\mathrm{W}=150, \mathrm{~L}=671$ |
| TL214, TL234 |  | $\mathrm{W} 1=0.000, \mathrm{~W} 2=0.000$, Offset $=-0.002$ | $\mathrm{W} 1=0, \mathrm{~W} 2=6$, Offset $=-97$ |
| TL215 | $0.117 \lambda, 10.17 \Omega$ | W = 13.208, L = 20.320 | W = 520, L=800 |
| TL216 | $0.014 \lambda, 38.82 \Omega$ | $\mathrm{W}=2.540, \mathrm{~L}=2.540$ | W = 100, L= 100 |
| TL217 | $0.013 \lambda, 51.58 \Omega$ | $\mathrm{W}=1.651, \mathrm{~L}=2.540$ | W = 65, L = 100 |
| TL218 | $0.012 \lambda, 10.17 \Omega$ | $\mathrm{W}=13.208, \mathrm{~L}=2.032$ | W = 520, L = 80 |
| TL219, TL232 | $0.014 \lambda, 23.03 \Omega$ | W = 5.080, L = 2.540 | W = 200, L = 100 |
| TL220 |  | $\begin{aligned} & \mathrm{W} 1=7.620, \mathrm{~W} 2=0.025, \mathrm{~W} 3=7.620 \\ & \mathrm{~W} 4=0.025 \end{aligned}$ | $\begin{aligned} & W 1=300, W 2=1, W 3=300, \\ & W 4=1 \end{aligned}$ |
| TL225, TL236 | $0.019 \lambda, 126.18 \Omega$ | $\mathrm{W} 1=0.152, \mathrm{~W} 2=0.152, \mathrm{~W} 3=3.810$ | $\mathrm{W} 1=6, \mathrm{~W} 2=6, \mathrm{~W} 3=150$ |
| $\begin{aligned} & \text { TL226, TL227, TL231, } \\ & \text { TL235, TL237 } \end{aligned}$ | $0.010 \lambda, 28.85 \Omega$ | $\mathrm{W} 1=3.810, \mathrm{~W} 2=3.810, \mathrm{~W} 3=1.829$ | $\mathrm{W} 1=150, \mathrm{~W} 2=150, \mathrm{~W} 3=72$ |
| TL228 (taper) | $0.036 \lambda, 16.47 \Omega / 38.82 \Omega$ | $\mathrm{W} 1=7.620, \mathrm{~W} 2=2.540, \mathrm{~L}=6.350$ | $\mathrm{W} 1=300, \mathrm{~W} 2=100, \mathrm{~L}=250$ |

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Reference Circuit (cont.)

## Circuit Assembly Information

Test Fixture Part No. LTN/PTFA091503E
Find Gerber files for this test fixture on the Infineon Web site at http://www.infineon.com/rfpower


Reference circuit assembly diagram (not to scale)*

Reference Circuit (cont.)

## Components Information

| Component | Description | Suggested Manufacturer | P/N |
| :--- | :--- | :--- | :--- |
| Input |  |  |  |
| C101, C102 | Chip capacitor, 33 pF | ATC | ATC100B330FW500XB |
| C103 | Chip capacitor, 5.1 pF | ATC | ATC100B5R1BW500XB |
| C104 | Chip capacitor, 7.5 pF | ATC | ATC100B7R5BW500XB |
| C105 | Chip capacitor, $4.71 \mu \mathrm{~F}$ | Digi-Key | PCS3475CT-ND |
| C106, C107 | Chip capacitor, $0.1 \mu \mathrm{~F}$ | ATC | 200B103MW |
| C108 | Chip capacitor, 4.7 pF | ATC | ATC100B4R7BW500XB |
| C801, C802, C803, C804, | Chip capacitor, $0.1 \mu \mathrm{~F}$ | Digi-Key | PCC104BCT-ND |
| C805 |  |  |  |
| R101, R103, R803 | Resistor, $10 \Omega$ | Digi-Key | P10ECT-ND |
| R102 | Resistor, $5100 \Omega$ | Digi-Key | P5.1KECT-ND |
| R801 | Resistor, $1300 \Omega$ | Digi-Key | P1.3KGCT-ND |
| R802 | Resistor, $1200 \Omega$ | Digi-Key | P1.2KGCT-ND |
| R804 | Resistor, $1200 \Omega$ | Digi-Key | P1.2KECT-ND |
| R805 | Resistor, $1000 \Omega$ | Digi-Key | P1.0KECT-ND |
| S1 | Transistor | Digi-Key | BCP5616TA-ND |
| S2 | Voltage Regulator | Digi-Key | LM78L05ACM-ND |
| S3 | Potentiometer, $2 \mathrm{k} \Omega$ | Digi-Key | 3224W-202ECT-ND |


| Output |  |  |  |
| :--- | :--- | :--- | :--- |
| C201, C204, C205, C210, <br> C211, C213, C214, C217 | Capacitor, $10 \mu \mathrm{~F}$ | Digi-Key | $587-1818-2-\mathrm{ND}$ |
| $\mathrm{C} 202, \mathrm{C} 216$ | Chip capacitor, 20000 pF | ATC | 200B203MW |
| $\mathrm{C} 203, \mathrm{C} 214$ | Chip capacitor, $1 \mu \mathrm{~F}$ | Digi-Key | $478-3993-2-\mathrm{ND}$ |
| C 206 | Chip capacitor, 33 pF | ATC | ATC100B330FW500XB |
| $\mathrm{C} 207, \mathrm{C} 215$ | Capacitor, $10 \mu \mathrm{~F}$ | Garrett Electronics | 281M5002106K |
| $\mathrm{C} 208, \mathrm{C} 209$ | Chip capacitor, 1.5 pF | ATC | ATC100B1R5BW500XB |

PTFA091503EL

## Package Outline Specifications

## Package H-33288-6



Diagram Notes—unless otherwise specified:

1. Interpret dimensions and tolerances per ASME Y14.5M-1994.
2. Primary dimensions are mm. Alternate dimensions are inches.
3. All tolerances $\pm 0.127$ [.005] unless specified otherwise.
4. Pins: $A=$ gate; $B=$ source; $C=$ drain; $D=$ drain voltage; $E, F=N . C$.
5. Lead thickness: $0.10+0.051 /-0.025$ [. $004+.002 /-.001]$.
6. Gold plating thickness: 0.25 micron [10 microinch] max.

Find the latest and most complete information about products and packaging at the Infineon Internet page http://www.infineon.com/rfpower

| Revision History: |  | 2010-08-11 |
| :--- | :--- | :--- |
| Previous Version: | 2010-07-26, Data Sheet | Data Sheet |
| Page | Subjects (major changes since last revision) |  |
| $5-9$ | Updated reference circuit information |  |
|  |  |  |
|  |  |  |
|  |  |  |

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highpowerRF@infineon.com
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+1877 465 3667 (1-877-GO-LDMOS) USA
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